Amendment

Amendment to Claims

Please amend or add the new claims as shown below.

7. (Once amended) An apparatus, comprising:

a static random access memory;

a processor coupled to the static random access memory;

a voltage regulator adapted to provide at least two voltage potential levels to at least a portion of the processor; and

wherein [the processor is adapted to adjust] the voltage potential level provided by the voltage regulator is adapted to be adjusted depending on the operational load of the processor.

9. (Once amended) The apparatus of claim 7, further comprising a state machine [adapted to determine] responsive to the operational load of the processor.

12. (Once amended) A method comprising:

determining an application [instruction] mix of a processor;

determining a frequency at which the processor may operate given the application [instruction] mix; and

determining a voltage potential level corresponding to the frequency; and

providing at least a portion of a processor with the frequency and voltage potential level.

13. (Once Amended) The method of claim 12, further comprising changing the frequency and voltage potential level in response to a change in the <u>application</u> [instruction] mix of the processor.

14. (Once Amended) A method of operating a processor, comprising:

adjusting the voltage potential level provided to at least a portion of the processor based on the <u>application</u> [instruction] mix executed by the processor.

15. (Once Amended) The method of claim 14, further comprising determining an operational frequency based on the <u>application</u> [instruction] mix executed by the processor.

17. (Once Amended) An article comprising: a storage medium having stored thereon instructions, that, when executed by a computing platform, results in:

adjusting the voltage potential level provided to at least a portion of the computing platform based on the <u>application</u> [instruction] mix executed by the computing platform.

18. (Once Amended) The article of claim 17, wherein the instructions, when executed, further result in determining a preferred operational frequency based on the <u>application</u> [instruction] mix executed by the computing platform.

19. (Once Amended) The article of claim 17, wherein the instructions, when executed, further result in computing platform executing the <u>application</u> [instruction] mix at peak performance.

22 (Newly added) A method comprising operating a core of a device at a

voltage independent of a voltage that operates a pad ring of the device.

23. (Newly added) The method of claim 22 further including operating the

core at a minimum supported voltage and operating the pad ring at a voltage

different from the core.

24. (Newly added) The method of claim 22 further including changing the

voltage to the core without changing the voltage to the pad ring.

25. (Newly added) An apparatus, comprising:

a processor;

a voltage regulator adapted to provide at least two voltage potential levels

to at least a portion of the processor; and

wherein the voltage potential level provided by the voltage regulator is

adapted to be adjusted depending on the operational load of the processor.

26. (Newly added) The apparatus of claim 25, wherein the voltage

regulator is further adapted to provide the voltage potential level depending on

an operational frequency of the processor.

27. (Newly added) The apparatus of claim 25, further comprising a state

machine responsive to an operational load of the processor.

28. (Newly added) The apparatus of claim 25, wherein said processor is

adapted to receive a clock signal that is varied in accordance with changes in

the operational load of said processor.

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29. (Newly added) A method comprising operating a processor core at a first voltage independent of a second voltage that operates a pad ring associated with the processor core, said first voltage being varied in accordance with changes in an operational load of said processor core.

30. (Newly added) The method of claim 29 further including operating the processor core at a minimum supported voltage and operating the pad ring at a voltage different from the processor core.

31. (Newly added) The method of claim 29 further including changing the first voltage to the processor core without changing the second voltage to the pad ring.